



AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Autonomous)

(Approved by A.I.C.T.E., New Delhi & Permanently Affiliated to JNTU-GV, Vizianagaram)

NAAC "A+" Accredited Institute

Cherukupally (Village), Near Tagarapuvalasa Bridge, Vizianagaram (Dist), A.P, Pin-531162.

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION

ENGINEERING

2nd BOARD OF STUDIES (BOS)-ECE MEETING MINUTES

The Meeting of the 2nd Board of Studies of Electronics and Communication Engineering, Avanthi Institute of Engineering and Technology (Autonomous), Cherukupally (Village), Near Tagarapuvalasa Bridge, Vizianagaram (Dist), AP, Pin-531162 was held on 30/05/2025 at 3:30 PM in Conference Hall, Ground Floor, AIET (A) and Zoom Link in Hybrid Mode.

Meeting Link:

<https://us06web.zoom.us/j/4280829918?pwd=Q5YbPJ6ATneqFUoUAlxZe6Rgd4mkH3.1&omn=81588121079>

Members Preset:

- 1. Dr. R.Gurunatha**
Associate Professor & CE, Department of Electronics and Communication Engineering,
JNTU-GV College of Engineering, JNTU-GV, Vizianagaram.
- 2. Dr.N.N.Murthy**
Professor & Dean (Student affairs),
Department of Electrical Engineering, Indian Institute of Technology (IIT), Tirupathi.
- 3. Dr. Krishna Chaitanya A**
Assistant Professor, Electronics and Communication Engineering,
National Institute of Technology (NIT), AP, Tadepalligudem.
- 4. Sri. B.S. Srinivas Rao**
AGM, BSNL, Vishakhapatnam
- 5. Putta Tejaswini**
Associate Software Engineer, Blue Cross Blue Shield, USA.
- 6. All the Faculty members,**
Department of Electronics and Communication Engineering
Avanthi Institute of Engineering and Technology (Autonomous)
- 7. Dr. G.Anantha Rao,**
Professor & Head, Chairperson, BoS,
Electronics and Communication Engineering
Avanthi Institute of Engineering and Technology (Autonomous)

Item 1:

Welcome Address by the Chairperson, BoS.



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Resolutions:

- The Chairperson of the Board of Studies (BoS), Dr. G.Anantha Rao, extended a warm welcome to all members of the BoS, including external experts, industry representatives, alumni, and faculty members, appreciating their continued support and commitment to academic excellence.
- Dr. Gandhi Satyanarayana, Dean–Academics, presented a summary of the Minutes of the 1st BoS Meeting and confirmed that the Regulations (R24), along with the Course Structure and Syllabus of:
 - I B.Tech (I & II Semesters) – Electronics and Communication Engineering, and
 - I M.Tech. (I & II Semesters) – VLSI Design was approved by the 1st Academic Council without any modifications.
- The Dean–Academics emphasized that the syllabus must adhere to the principles of Outcome-Based Education (OBE), a mandatory component in light of the revised NAAC Accreditation Framework and the National Education Policy (NEP) 2020.
- With the consent of the BoS members, the Chairperson confirmed the successful implementation of the R24 Curriculum and Syllabi for the 2024–2025 admitted batch, covering I B.Tech and I M.Tech (I & II Semesters).
- The Chairperson also outlined the agenda for the meeting, stressing the importance of collaborative academic planning to ensure the curriculum remains responsive to emerging technologies and industrial needs.
- All BoS members unanimously expressed their commitment to leveraging their collective expertise in developing a curriculum and syllabi that:
 - Fulfill the standards of Outcome-Based Education, and
 - Address the evolving expectations of industry and society.

Item 2:

To review and approve the proposed curriculum for B.Tech. Electronics and Communication Engineering-II Year (I & II Semesters), effective from the academic year: 2025–2026.

Members Suggestions:

- The members of the Board of Studies thoroughly reviewed the proposed curriculum for II Year – I & II Semesters.



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- Members suggested changing the lab name from Analog Electronic circuits lab to Analog Electronic Circuits Design lab II Year – II Semester.
- Members also enquired about the **introduction timeline for the “Electromagnetic waves and transmission lines” course.**

Resolutions:

- As per BoS member’s suggestions, the Chairperson assured to change the name of **Analog Electronic Circuits lab to Analog Electronic Circuits Design lab in II Year – II Semester.**
- The Chairperson assured the Board that the **“Electromagnetic waves and transmission lines” course is scheduled to be introduced in the III Year – I Semester.**
- After **detailed deliberations**, the **Board unanimously approved the proposed curriculum** for B.Tech Electronics and Communication Engineering – II Year (I & II Semesters), effective from the **academic year 2025–2026.**

Item 3:

To review and approve the proposed curriculum for **M.Tech. VLSI Design – III & IV Semesters**, effective from the academic year 2025–2026.

Members Suggestions:

- The **members of the Board of Studies** thoroughly reviewed the proposed curriculum for **M.Tech. VLSI Design – III & IV Semesters** effective from the academic year **2025–2026.**
- Members suggested including MOOC’s /NPTEL Courses with course code in the curriculum for professional elective-V and opening **Elective-I in III Semester.**

Resolutions:

- The Chairperson assured the Board that to add the **MOOC’s/NPTEL Courses** with course code in the curriculum for **Professional elective-V and Open Elective-I in II Year – I Semester.**
- After thorough discussion and review, the members **approved** the proposed curriculum for **M.Tech. VLSI Design – III & IV Semesters**, effective from the academic year 2025–2026.

Item 4:

To review and approve the Guidelines for the **Honors Programme in VLSI Design and Technology (R24)** for students admitted from the academic year 2024-2025 onwards.

Members Suggestions:



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- **The Chairperson, BOS,** explained that the titles and structure of the above programs align with the AICTE Hand Book (2024–2027) and follow the guidelines issued by JNTU-GV **vide reference: JNTUGV/DAP/Guidelines for B.Tech Honors/2025, dated 20-02-2025.**
- Members are suggested to encourage students to learn through Blended Learning and NPTEL/MOOC courses.
- For the Honors programs, each student is required to earn an additional 18 credits, over and above the mandatory **160 credits for the B.Tech degree**, to qualify for the Honors degree.

Resolutions:

- After thorough discussion and review, the members **approved** the proposed **Guidelines for the Honors Programme in VLSI Design and Technology (Regulations – R24)**, effective from the academic year 2025–2026.

Item 5:

To review and approve the Guidelines for the **Minor Program: Sensors Technology (R24)** for students admitted from the academic year 2024-2025 onwards.

Members Suggestions:

- The **Dean–Academics** clarified that the Honors and Minor programmes are designed as per the **AICTE Handbook** and aligned with the **JNTU-GV Guidelines** issued under Ref: *JNTUGV/DAP/Guidelines for B.Tech Honors/2025, dated 20-02-2025.*
- Members are expressed satisfaction Minor Courses and syllabus.

Resolutions:

- After thorough discussion and review, the members **approved** the proposed Guidelines for the Minor Programme in **Sensors Technology (Regulations – R24)**, effective from the academic year 2025–2026.

Item 6:

To discuss and finalize the syllabus for **II B.Tech. – I Semester** (Electronics and Communication Engineering), for the following courses:

- i. Random Variables and Stochastic Processes (R24ECPC03)
- ii. Signals and Systems (R24ECPC04)
- iii. Electronic Devices and Circuits (R24ECPC05)
- iv. Digital Circuit Design (R24ECPC06)



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v. Electronic Devices and Circuits Lab (R24ECPC07)

vi. Digital Circuit Design lab (R24ECPC08)

Members Suggestions:

- For **Random Variables and Stochastic Processes (R24ECPC03)**: Ensure that to add the fundamental probability concepts in **Unit-I**.
- For all courses: **Update textbook references to the latest editions** and include **relevant NPTEL web links** to enhance accessibility and promote self-directed learning.

Resolution:

- The Board considered and accepted the above suggestions.
- The syllabus for all the listed courses under **II B.Tech – I Semester** was **approved**, subject to the **incorporation of the suggested modifications** and updates.

Item 7:

To discuss and finalize the syllabus for **II B.Tech. – II Semester** (Electronics and Communication Engineering) for the following courses:

- i. Linear IC Applications (R24ECPC09)
- ii. Advanced Control Systems (R24ECPC10)
- iii. Analog Electronic Circuits Design (R24ES10)
- iv. Analog and Digital Communications (R24ECPC11)
- v. Analog Electronic Circuits Design Lab (R24ECPC12)
- vi. Analog and Digital Communications Lab (R24ECPC13)

Members Suggestions:

- For Linear IC Applications (R24ECPC09): Ensure that to add a topic-Design using Sallen-Key Topology in Unit-III
- Analog and Digital Communications (R24ECPC11): Ensure that to include a self learning topic-Need for limiters in FM receivers in Unit-II.
- For all courses: **Update the textbook references** to include the **latest editions**.

Resolution:

- The Board considered and accepted the above suggestions.
- The syllabus for all the listed courses under **II B.Tech – II Semester** was **approved**, subject to the **incorporation of the suggested modifications** and updates.



Item 8:

To discuss and finalize the **Syllabus of M.Tech III and IV Semester (VLSI Design)** for the following:

- i. Nano Materials and Nano Technology (MTVDPE21051)
- ii. Digital System Design and Verification (MTVDPE21052)
- iii. Photonics (MTVDPE21053)
- iv. Dissertation Phase-I /Industrial Project (MTVDPJ2101)
- v. Fundamentals of Nano Technology (MTVDOE2101)
- vi. Hardware Software Co-Design (MTVDOE2102)
- vii. Digital CMOS VLSI Design (MTVDOE2103)
- viii. Embedded Systems (MTVDOE2104)
- ix. Project/ Dissertation Phase-II (MTVDPJ2201)

Members Suggestions:

- Members recommended encouraging students to enroll in MOOC's through NPTEL/SWAYAM platforms, in accordance with AICTE and JNTU-GV guidelines.
- It was suggested that, as part of the Final Project, students should:
 - Take up a **relevant NPTEL course**.
 - Align their final project work with the MOOC topic.
 - Be encouraged to **publish a research paper** based on the project.

Resolution:

- The BoS Chairperson assured the Board that the department will ensure student enrollment in MOOC courses, integrating them as part of Professional Electives V and Open Electives.
- The department will also promote the application of MOOC learnings in final project work and encourage scholarly publications.
- The Board unanimously approved the syllabus for the above listed courses for M.Tech – III & IV Semesters (VLSI Design), effective from the academic year 2025–2026.

Item 9:

To discuss and finalize the syllabus for the **Honors Programme in VLSI Design and Technology**:

- i. CMOS Digital IC Design (R24VDT201)
- ii. System On Chip Design (R24VDT301)



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iii. Nano Technology (R24VDT302)

iv. Photonics Engineering (R24VDT401)

Members Suggestions:

- Members recommended that the department should actively encourage students to enroll in the Honors and Minor Programmes to enhance their technical competencies and specialization in emerging areas.

Resolution:

- The Board appreciated the structure and relevance of the proposed courses and emphasized the importance of promoting Honors/Minors enrollment among eligible students.
- The Board of Studies unanimously approved the syllabus for the Honors Programme in VLSI Design and Technology (Regulations – R24), effective for students admitted from the academic year 2024–2025 onwards.

Item 10:

To discuss and finalize the syllabus for the Minor Programme in Sensors Technology:

- i. Principles of Sensors (R24STT201)
- ii. Sensors and Actuators (R24STT301)
- iii. Basics of Embedded Systems (R24STT302)
- iv. Introduction to Internet of things (R24STT401)

Members Suggestions:

- Members recommended that the department should actively encourage students to enroll in the Honors and Minor Programmes to enhance their technical competencies and specialization in emerging areas.

Resolution:

- The Board appreciated the structure and relevance of the proposed courses and emphasized the importance of promoting Honors/Minors enrollment among eligible students.
- The Board unanimously approved the syllabus for the Minors Programme in Sensors Technology (Regulations – R24), effective for students admitted from the academic year 2024–2025 onwards.



Item 11:

To suggest improvements in **Teaching-Learning Methodologies**, and finalize **Co-Curricular and Extra-Curricular Activities** for the academic year **2025–2026**.

Members' Suggestions:

- The **Chairperson, BoS**, informed the Board that the **R24 Regulations**, along with the revised course structure and syllabus, are to be implemented for students admitted in both **2024–2025 and 2025–2026 batches**.
- Members concurred with this implementation plan and recommended that it be **forwarded for ratification in the forthcoming Academic Council Meeting**.
- Members suggested encouraging students to **enroll in online internships** offered by reputed **industries and platforms**, particularly those aligned with their academic curriculum, to enhance **practical exposure and employability**.
- The Board emphasized the importance of implementing a **diverse range of co-curricular activities**, including: Guest Lectures, Tech Fests, Workshops, Hackathons, Industrial Visits
- Integration of **emerging technologies**, particularly those related to **Artificial Intelligence (AI)**, was recommended. Suggested topics include: Robotics with AI and Artificial Intelligence & Machine Learning (AIML) applications in industrial systems

Resolution:

- The Board **appreciated the initiatives** presented and **approved the proposed Teaching-Learning Methodologies** along with the **suggested Co-Curricular and Extra-Curricular Activities** for the **academic year 2025–2026**.
- It was further resolved that **faculty members be encouraged to integrate AI-related emerging topics** into their teaching methods and student engagement practices to **enhance industry relevance and prepare students for future challenges**.
- The **implementation plan for R24 Regulations** for both **2024–2025 and 2025–2026 admitted batches** shall be **forwarded to the Academic Council for formal ratification**.

Item 12:

To discuss any other academic matters with the permission of the Chair.

Members' Suggestions:

- Members emphasized the importance of initiating **Campus Recruitment Training (CRT)**



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from the first year onwards. Early exposure to CRT will enable students to be well-prepared for campus recruitment processes by their final year.

- The Board of Studies advised the department to **strengthen placement activities** with a focused strategy to **increase the number of student selections** in campus recruitment drives. Key suggestions included:
 - Enhancing **industry interactions** through collaborative activities, seminars, and networking.
 - Organizing **mock interviews, technical tests, and group discussions** regularly.
 - Encouraging students to maintain **technically sound resumes**, and participate in **competitive platforms**.
- The Board also recommended that the department should **strategically plan and implement placement enhancement initiatives** to maximize student success in upcoming recruitment drives.

Resolution:

- The Board of Studies appreciated the importance of early placement readiness and advised the department to initiate CRT programs from the first year itself.
- The Board resolved that the department shall take proactive measures to implement the above recommendations and strengthen student placement outcomes through structured and sustained efforts.

Item 13:

Any miscellaneous issues.

Members' Suggestions:

- Members emphasized the importance of conducting **periodic reviews of the curriculum and syllabus** to ensure alignment with **emerging technologies** and the latest AICTE/UGC guidelines.
- It was suggested that the department should **increase the frequency of guest lectures, seminars, webinars, and industry expert talks** to help bridge the **gap between academic content and industry expectations**.

Resolution:

- The Board agreed on the necessity for continuous curriculum review and recommended that such reviews be scheduled systematically to keep the programs relevant and future-focused.



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- The department was advised to **organize more academic-industry engagement activities**, including expert sessions and knowledge-sharing forums, to enhance students' exposure to real-world industrial practices.

Attendance Sheet

S No.	Name & Affiliation	Signature
1	Dr.R.Gurunadha Associate Professor & CE Department of Electronics and Communication Engineering, JNTU-GV College of Engineering, JNTU-GV, Vizianagaram.	 30/05/25
2	Dr.N.N.Murthy Professor & Dean (Student affairs), Department of Electrical Engineering, Indian Institute of Technology (IIT), Tirupathi.	Approved through mail on 11/6/2025
3	Dr.Krishna Chaitanya. A Assistant Professor Department of Electronics and Communication Engineering, National Institute of Technology (NIT), AP- Tadepalligudem.	Approved through mail on 6/6/2025
4	Sri.B.S.Srinivas Rao, AGM, BSNL Vishakhapatnam	Approved through mail on 5/6/2025
5	Putta Tejaswini Associate Software Engineer, Blue CrossBlue Shield, USA Number: 15Q71A0456	Approved through mail. @
6	Dr.Gandi Satyanarayana Dean-Academics and Professor of CSE Avanthi Institute of Engineering and Technology (Autonomous)	
7	All the Department faculty Department of Electronics and Communication Engineering	



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8	Dr. G. Anantha Rao Professor & Head Chairperson, BoS Department of Electronics and Communication Engineering Avanthi Institute of Engineering and Technology (Autonomous)	G. Att

Dept. Members

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3. P. P
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5. K. Srinivasa
6. B. A
7. J. J
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9. M. Anushay
10. S. S. Bhavan
11. Ch. Venkata
12. M. V
13. G. Bhawan
14. K. Srinivasa
15. S. Srinivasa
16. R. R

G. Att
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Board of Studies (ECE)

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