



AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Autonomous)

(Approved by A.I.C.T.E., New Delhi & Permanently Affiliated to JNTU-GV, Vizianagaram)

NAAC "A+" Accredited Institute

Cherukupally (Village), Near Tagarapuvalasa Bridge, Vizianagaram (Dist), AP, Pin-531162.

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I M.Tech II Semester Regular Examinations (R24), July-2025

Time Table

Academic Year: 2024-2025

Regulation: R24

Date: 01-07-2025

Branch	DATE & DAY			
	17-07-2025 (Thursday)	18-07-2025 (Friday)	19-07-2025 (Saturday)	21-07-2025 (Monday)
	1 PM to 4 PM	1 PM to 4 PM	1 PM to 4 PM	1 PM to 4 PM
M.Tech Computer Science & Engineering	Advanced Computer Networks (MTCS1201)	Mean Stack Technologies (MTCS1202)	Advanced Database and Mining (MTCS12032)	Social Network Analysis (MTCS12042)
M.Tech VLSI Design	Mixed Signal & RF IC Design (MTVDPC1205)	Physical Design Automation (MTVDPC1206)	IOT & its Applications (MTVDPE12032)	Embedded System Design (MTVDPE12041)
M.Tech CAD/CAM Engineering	Theory of Elasticity and Plasticity (R24CCPC05)	Advanced Manufacturing Processes (R24CCPC06)	Product Design and Development (R24CCPE09)	Additive Manufacturing (R24CCPE12)
M.Tech Power Electronics	Advanced Power Electronic Converters-II (MTPE1201)	Power Electronic Control of Electrical Drives (MTPE1202)	Electric Vehicles and Design (MTPE12032)	Applications of Power Converters (MTPE12042)

H. Praveen
ACE

COE
11/7/25

Controller of Examinations
Avanathi Institute of Engineering and Technology

Principal
11/7/25

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